

Schedule
Faculty of Electrical Engineering

Subject	Hours weekly	ECTS	Lecturer	Email	Date	Students
High Voltage Engineering	2L, 2Lab	4	Prof. K. Chwastek Prof. M. Najgebauer	krzysztof.chwastek@gmail.com mariusz.najgebauer@pcz.pl	Lec, Wed 12:15-14:00, E4, FEE Lab, Mon 11:15-13:00, F212, FEE	
Digital Signal Processing	2L, 2Lab	4	Dr J. Baran	baranj@el.pcz.czyst.pl	Thu, 9:15-13:00, B231, FEE	
Electrical Engineering - Circuit Theory 1	2L, 2C	6	Prof. P. Jabłoński Dr A. Zaremba	p.jablonski@el.pcz.czyst.pl zaremba@el.pcz.czyst.pl	Lec, Tue 8:15-10:00, E9, FEE C, Mon 13:15-15:00, E5, FEE	
Computer Architecture	2L, 1Lab	4	Prof. S. Gryś	grys@el.pcz.czyst.pl	Fri 11:15-14:00, B031, FEE	
Modelling and Simulation	2L, 2Lab	4	Dr K. Olesiak	kolesiak@el.pcz.czyst.pl	Wed 14:15-18:00, B231, FEE	
Numerical Methods	1L, 2Lab	5	Prof. K. Chwastek	krzysztof.chwastek@gmail.com	Wed 9:15-12:00, E113, FEE	
Programming in JAVA	1L, 2Lab	4	Dr Ł. Piątek	l_piatek@el.pcz.czyst.pl	Thu, 13:15-16:00, IK2, FEE	
Image processing and recognition	1L, 2Lab	3	Prof. S. Gryś	grys@el.pcz.czyst.pl	Fri 8:15-11:00, B231, FEE	